

PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Devendra K. Sadana, et al.

Examiner: Unassigned

Serial No: Unassigned

Art Unit: Unassigned

Filed: Herewith

Docket: YOR920010499US1 (14710)

For: METHOD OF CREATING
HIGH-QUALITY RELAXED
SiGe-ON-INSULATOR FOR STRAINED
Si CMOS APPLICATIONS

Dated: January 23, 2002

Assistant Commissioner for Patents
United States Patent and Trademark Office
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the following references, which are also listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. U.S. Patent No. 5,461,243, dated October 24, 1995, issued to Ek, et al.;
2. U.S. Patent No. 5,563,428, dated October 8, 1996, issued to Ek, et al.;

CERTIFICATE OF MAILING BY EXPRESS MAIL

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Date of Deposit: **January 23, 2002**

I hereby certify that this correspondence is being deposited with the United States Postal Service Express Mail Post Office to Addressee service under 37 C.F.R. §1.10 on the date indicated above and is addressed to the Assistant Commissioner of Patents and Trademarks, Washington, D.C. 20231.

Dated: January 23, 2002

Mishelle Mustafa

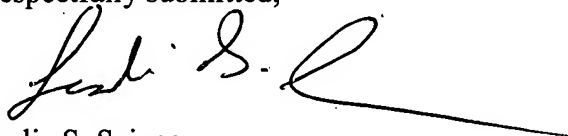
3. U.S. Patent No. 5,667,586, dated September 16, 1997, issued to Ek, et al.;
and

4. U.S. Patent No. 5,759,898, dated June 2, 1998, issued to Ek, et al.

Applicants are submitting copies of the above-cited references.

Inasmuch as this Information Disclosure Statement is being submitted in
accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Leslie S. Szivos', with a long horizontal flourish extending to the right.

Leslie S. Szivos
Registration No. 39,394

SCULLY, SCOTT, MURPHY & PRESSER
400 Garden City Plaza
Garden City, New York 11530
(516) 742-4343

LSS:tt